

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Kei WATANABE, et al.) **Parent** Group Art Unit: 2812
)
Div. of Application No.: 10/201,892 filed) **Parent** Examiner: Nguyen, Ha T.
July 25, 2002)
)
Filed: Herewith)
)
For: SEMICONDUCTOR DEVICE AND)
MANUFACTURING METHOD)
THEREOF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

PRELIMINARY AMENDMENT

Please amend the above-identified application as set forth in this paper.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 6 of this paper.

FINNEGAN
HENDERSON
FARABOW
GARRETT &
DUNNER ^{LLP}

1300 I Street, NW
Washington, DC 20005
202.408.4000
Fax 202.408.4400
www.finnegan.com